

Aleksandar Radonjić

Full Professor

Faculty of Informatics and Computing
University „Union – Nikola Tesla“
st. Cara Dušana 62-64, Belgrade
e-mail: aradonjic@unt.edu.rs
tel.: +381-607-074-708



EDUCATION

- 2013. Ph.D. in Electrical and Computer Engineering, Faculty of Technical Sciences, University of Novi Sad.
- 2004. Dipl. Eng. in Electrical and Computer Engineering – Instrumentation, Faculty of Technical Sciences, University of Novi Sad.
- 2004. Dipl. Eng. in Electrical and Computer Engineering – Telecommunications, Faculty of Technical Sciences, University of Novi Sad.

TEACHING/ACADEMIC TITLES

- 2024. Full Professor, Narrow Scientific Field: Informatics, Faculty of Informatics and Computing, University „Union – Nikola Tesla“, Belgrade.
- 2020. Senior Research Associate, Narrow Scientific Field: Electronics, Telecommunications and Information Technologies, Institute of Technical Sciences of the Serbian Academy of Sciences and Arts.
- 2020. Associate Professor, Narrow Scientific Field: Information Technologies, Faculty of Management Sremski Karlovci, University „Union – Nikola Tesla“, Belgrade.
- 2015. Research Associate, Narrow Scientific Field: Electronics, Telecommunications and Information Technologies, Institute of Technical Sciences of the Serbian Academy of Sciences and Arts.
- 2015. Assistant Professor, Narrow Scientific Field: Information Technologies, Faculty of Management Sremski Karlovci, University „Union – Nikola Tesla“, Belgrade.

PROFESSIONAL EXPERIENCE

- 2024. – Faculty of Informatics and Computing, University „Union – Nikola Tesla“, Full Professor.
- 2020. – Institute of Technical Sciences of the SASA, Senior Research Associate.
- 2020. – 2024. Faculty of Management, University „Union – Nikola Tesla“, Associate Professor.
- 2015. – 2020. Institute of Technical Sciences of the SASA, Research Associate.
- 2015. – 2020. Faculty of Management, University „Union – Nikola Tesla“, Assistant Professor.
- 2014. – 2015. Faculty of Management, University „Union – Nikola Tesla“, Lecturer.
- 2008. – 2009. Faculty of Technical Sciences, University of Novi Sad, Project Specialist.

MEMBERSHIPS

- 2012. – IEEE (member card: 90517479)

PAPERS IN JOURNALS WITH IMPACT FACTOR

1. **Aleksandar Radonjic**, Pankaj Kumar Das, Vladimir Vujicic: "Integer Codes Correcting Burst Error within One Byte and Single Errors within Two Bytes," *Cryptography and Communications*, vol. 16, no. 5, pp. 961-974, Sept. 2024.
<https://doi.org/10.1007/s12095-023-00687-7>
IF(2023): 1.2
2. **Aleksandar Radonjic**, Pankaj Kumar Das, Nabin Kumar Pokhrel: "Integer Codes Correcting Single Adjacent Transposition Errors," *IEEE Communications Letters*, vol. 28, no. 7, pp. 1499-1503, Jul. 2024.
<https://doi.org/10.1109/LCOMM.2024.3379493>
IF(2023): 3.7
3. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting/Detecting Burst Errors within One Byte and Detecting Burst Errors within Two Bytes", *Computational and Applied Mathematics*, vol. 43, no. 2, Feb. 2024.
<https://doi.org/10.1007/s40314-023-02435-2>
IF(2023): 2.5
5. **Aleksandar Radonjic**, Vladimir Vujicic: "Logarithmic Time Encoding and Decoding of Integer Error Control Codes," *Engineering Reports*, vol. 5, no. 1, e12675, Nov. 2023.
<https://doi.org/10.1002/eng2.12675>
IF(2023): 1.8
6. Miljan Miletic, **Aleksandar Radonjic**: "Integer Codes Correcting Single Errors and Double Adjacent Errors", *Electronics Letters*, vol. 59, no. 20, article no. e12973, Oct. 2023.
<https://doi.org/10.1049/ell2.12973>
IF(2023): 0.7
7. **Aleksandar Radonjic**, Igor Ristic, Ivan Scepanovic: "Integer Codes Correcting Single Errors and Detecting Burst Errors within Two Bytes", *IET Communications*, vol. 17, pp. 1255-1261, July 2023.
<https://doi.org/10.1049/cmu2.12620>
IF(2023): 1.5
8. **Aleksandar Radonjic**, Nabin Kumar Pokhrel, Pankaj Kumar Das: "Integer Codes Correcting Burst Errors within Two Bytes", *Computational and Applied Mathematics*, vol. 42, no. 3, Apr. 2023.
<https://doi.org/10.1007/s40314-023-02225-w>
IF(2023): 2.5
9. Nabin Kumar Pokhrel, Pankaj Kumar Das, **Aleksandar Radonjic**: "Integer Codes Capable of Correcting Burst Asymmetric Errors", *Journal of Applied Mathematics and Computing*, vol. 69, no. 1, pp. 771-784, Feb. 2023.
<https://doi.org/10.1007/s12190-022-01770-7>
IF(2023): 2.4
10. Miodrag Mihaljevic, **Aleksandar Radonjic**, Lianhai Wang, Shujiang Xu: "Security Enhanced Symmetric Key Encryption Employing an Integer Code for the Erasure Channel", *Symmetry*, vol. 14, no. 8, p. 1709, Aug. 2022.
<https://doi.org/10.3390/sym14081709>
IF(2022): 2.7
11. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Burst Asymmetric Errors within a Byte", *IETE Journal of Research*, vol. 68, no. 1, pp. 176-182, Jan. 2022.
<https://doi.org/10.1080/03772063.2019.1593056>
IF(2022): 1.5

12. **Aleksandar Radonjic**: "Integer Codes Correcting Single Errors within Two Bytes", *Journal of Circuits, Systems and Computers*, vol. 30, no. 14, paper no. 2150260, Nov. 2021.
<https://doi.org/10.1142/S0218126621502601>
 IF(2021): 1.278
13. **Aleksandar Radonjic**: "Integer Codes Correcting Single Asymmetric Errors", *Annals of Telecommunications*, vol. 76, nos. 1-2, pp. 109-113, Jan. 2021.
<https://doi.org/10.1007/s12243-020-00816-w>
 IF(2021): 1.901
14. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Single Errors and Random Asymmetric Errors within a Byte", *Journal of Systems Science and Complexity*, vol. 33, no. 6, pp. 2103-2113, Dec. 2020.
<https://doi.org/10.1007/s11424-020-8145-9>
 IF(2021): 1.260
15. **Aleksandar Radonjic**: "Integer Codes Correcting Single Errors and Detecting Burst Errors within a Byte", *IEEE Transactions on Device and Materials Reliability*, vol. 20, no. 4, pp. 748-753, Oct. 2020.
<https://doi.org/10.1109/TDMR.2020.3033511>
 IF(2020): 1.761
16. **Aleksandar Radonjic**: "Integer Codes Correcting Double Errors and Triple-Adjacent Errors within a Byte", *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 28, no. 8, pp. 1901-1908, Aug. 2020.
<https://doi.org/10.1109/TVLSI.2020.2998364>
 IF(2020): 2.312
17. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Burst Asymmetric Errors within a Byte and Double Asymmetric Errors," *Cryptography and Communications*, vol 12, no. 2, pp. 221-230, Mar. 2020.
<https://doi.org/10.1007/s12095-019-00388-0>
 IF(2020): 1.367
18. Vladimir Vujicic, Boris Licina, Dragan Pejic, Platon Sovilj, **Aleksandar Radonjic**: "Stochastic Measurement of Wind Power Using a Two-bit A/D Converter", *Measurement*, vol. 152, paper no. 107184, Feb. 2020.
<https://doi.org/10.1016/j.measurement.2019.107184>
 IF(2020): 3.927
19. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Sparse Byte Errors," *Cryptography and Communications*, vol. 11, no. 5, pp. 1069-1077, Sep. 2019.
<https://doi.org/10.1007/s12095-019-0350-9>
 IF(2019) = 1.291
20. Dragan Pejic, Dragana Naumovic-Vukovic, Bojan Vujicic, **Aleksandar Radonjic**, Platon Sovilj, Vladimir Vujicic: "Stochastic Digital DFT Processor and Its Application to Measurement of Reactive Power and Energy", *Measurement*, vol. 124, pp. 494-504, Aug. 2018.
<https://doi.org/10.1016/j.measurement.2018.04.004>
 IF(2018): 2.791
21. **Aleksandar Radonjic**: "(Perfect) Integer Codes Correcting Single Errors," *IEEE Communications Letters*, vol. 22, no. 1, pp. 17-20, Jan. 2018.
<https://doi.org/10.1109/LCOMM.2017.2757465>
 IF(2018) = 3.457

22. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Burst and Random Asymmetric Errors within a Byte," *Journal of the Franklin Institute*, vol. 355, no. 2, pp. 981-996, Jan. 2018.
<https://doi.org/10.1016/j.jfranklin.2017.11.033>
 IF(2018) = 3.653
23. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Single Errors and Burst Asymmetric Errors within a Byte," *Inform. Processing Letters*, vol. 121, pp. 45-50, May 2017.
<https://doi.org/10.1016/j.ipl.2017.01.010>
 IF(2017) = 0.793
24. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting High-Density Byte Asymmetric Errors," *IEEE Communications Letters*, vol. 21, no. 4, pp. 694-697, Apr. 2017.
<https://doi.org/10.1109/LCOMM.2016.2644661>
 IF(2017) = 2.723
25. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Spotty Byte Asymmetric Errors," *IEEE Communications Letters*, vol. 20, no. 12, pp. 2338-2341, Dec. 2016.
<https://doi.org/10.1109/LCOMM.2016.2598803>
 IF(2016) = 1.988
26. **Aleksandar Radonjic**, Karlo Bala, Vladimir Vujicic: "Integer Codes Correcting Double Asymmetric Errors," *IET Communications*, vol. 10, no. 14, pp. 1691-1696, Sep. 2016.
<http://doi.org/10.1049/iet-com.2016.0097>
 IF(2016) = 1.061
27. **Aleksandar Radonjic**, Platon Sovilj, Vladimir Vujicic: "Stochastic Measurement of Power Grid Frequency Using a Two-Bit A/D Converter", *IEEE Transactions on Instrumentation and Measurement*, vol. 60, pp. 56-62, Jan. 2014.
<http://doi.org/10.1109/TIM.2013.2277515>
 IF(2014): 1.790
28. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer Codes Correcting Burst Errors within a Byte", *IEEE Transactions on Computers*, vol. 62, pp. 411-415, Feb. 2013.
<http://doi.org/10.1109/TC.2011.243>
 IF(2013): 1.473
29. **Aleksandar Radonjic**, Vladimir Vujicic: "Integer SEC-DED Codes for Low Power Communications", *Information Processing Letters*, vol. 110, pp. 518-520, June 2010.
<http://doi.org/10.1016/j.ipl.2010.04.004>
 IF(2010) = 0.612.

CITATIONS

Google Scholar: 301, *h*-index: 11, <https://scholar.google.com/citations?user=Jb70UC8AAAAJ&hl=sr>
 Scopus: 212, *h*-index: 10, <https://www.scopus.com/authid/detail.uri?authorId=56271592800>
 Web of Science: 177, *h*-index: 9, <https://www.webofscience.com/wos/author/record/JMQ-1892-2023>

REVIEWER FOR JOURNALS

IEEE Transactions on Communications, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, IEEE Transactions on Parallel and Distributed Systems, IEEE Transactions on Information Theory, IEEE Communications Letters, Mathematics, AIMS Mathematics, Cryptography and Communications, Advances in Mathematics of Communications, etc.

OTHER LINKS

<https://enauka.gov.rs/cris/rp/rp09243>

<https://orcid.org/0000-0003-3715-468X>

<https://www.researchgate.net/profile/Aleksandar-Radonjic>

<https://ieeexplore.ieee.org/author/38467075000>

<https://link.springer.com/search?dc.creator=Aleksandar%20Radonjic>